Final Presentation (TSUG) notes

- Main controller block missing from software architecture diagram

- Delays needed in several loops

- Averaging of sensor readings could possibly be done in monitoring system loop instead of control loop

- Tasks created and started in LabVIEW rather than using MAX

- PID Control and Hardware interface could be combined into one loop, isolated by case structure

- Averaging needed for temp reading

- On/Off sequence structure for heater only needs one wait delay

- Level sensor circuit tested in water/against variable capacitors

- UI tabs

- Filtration components needed